



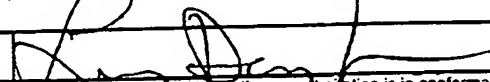


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Substitute for form 1449A/PTO <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (use as many sheets as necessary)		<b>Complete if Known</b>			
		Application Number			
		Filing Date			
		First Named Inventor	William C. Moyer		
		Group Art Unit			
		Examiner Name	Unknown		
Sheet	2	of	2	Attorney Docket Number	SC90674A CD1

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
	AU	Motorola, Inc. 1990, "M68300 Family CPU32 Central Processor Unit Reference Manual", Section 7 Development Support, pgs. 7-1 through 7-31.	
	AV	Motorola, Inc. 1995, "DSP56300 24-Bit Digital Signal Processor Family Manual", Section 10 On-Chip Emulator (OnCE™), pgs. 10-1 through 10-29.	
	AW	Motorola, Inc. 1995, "DSP56300 24-Bit Digital Signal Processor Family Manual", Section 11 JTAG (IEEE 1149.1) Test Access Port, pgs. 11-1 through 11-9.	
	AX	S.B. Furber: "Comprocessor Data Transfer Instructions", VLSI RISC Architecture and Organization, pgs. 262-265, XP-002061358 ISBN 0-8247-8151-1. 1989	

Examiner Signature		Date Considered	05/24/04
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation, if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> Unique citation designation number. <sup>2</sup> Applicant is to place a check mark here if English Language Translation is attached.